

PART B - 03-02-09
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021567 7590 12/08/2003

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/092,875	03/05/2002	Paul A. Morgan	M122-1798	9981

TITLE OF INVENTION: ETCHING PROCESSES FOR INTEGRATED CIRCUIT MANUFACTURING INCLUDING METHODS OF FORMING CAPACITORS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	03/08/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
KILDAY, LISA A	2829	438-396000

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Micron Technology, Inc.

Boise, Idaho

Please check the appropriate assignee category or categories (will not be printed on the patent): individual corporation or other private group entity government

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03/11/2004 WASFAN2 00000057 10092875

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